```
  ####     ####    #    #    #####   ######   #    #    #####    ####
 #    #   #    #   ##   #      #     #        ##   #      #     #
 #        #    #   # #  #      #     #####    # #  #      #      ####
 #        #    #   #  # #      #     #        #  # #      #          #
 #    #   #    #   #   ##      #     #        #   ##      #     #    #
  ####     ####    #    #      #     ######   #    #      #      ####
```

Table of Contents

Date: Mon, 13 Mar 1995 18:40:59 -0800
From: dmk@hsmpk14a-91 (David Kahn)
Subject: Item #250: Procedural: rules for email ballot

P1275 Open Firmware Working Group Proposal -- Proposal #250 Ver 1

Title: Email balloting procedure

Author: David Kahn

Date: March 13, 1995

Ed/Tech: Procedural

Synopsis: Open Firmware Working Group Email Ballot Procedure

Doc & Version: n/a

Problem: Fulfill action item to define email ballot procedure.

Proposal:

I was tasked with coming up with the process, and here's the process proposal. Please comment within 5 days on this email process and we can try the process with this item first, and as a matter of course, reaffirm the items and this process at the next meeting:

Create an email ballot administrator, assign the role to the vice chair initially.

Somebody sends a proposal in and requests email balloting. Any eligible member can request email balloting of a properly submitted proposal.

If there are no objections by eligible voters within 5 full business days, starting the business day after the proposal is posted by the proposal number generator, the ballot item is eligible for email ballot. The administrator declares an item eligible for email balloting. The discussion period on an email ballot runs simultaneously with this 5 day period.

The admin sets up an alias for email balloting. (Publicized by the admin at the time the voting is opened on a proposal). The alias may be different for each ballot, to make it easier for the admin to track the ballot items.

Votes are sent in to this alias and tabulated by the admin who produces a summary report at the end of the ballot period showing each person's vote. If you vote more than once, only the last vote you sent in, determined by the time you sent in the vote, counts. Previous votes are discarded. The email headers are used to determine the correct vote. In the event the admin is notified of header forgery, or header forgery becomes apparent within one week following a vote, the vote is declared void.

Eligible voters that "know" they will be on vacation can send in their vote early to the admin, with specific instructions on their vote. The administrator should try to do his/her best to include the vote in the official tally, but is not required to do so. If for some reason the vote cannot be included, the admin will post a message to the email alias indicating why the vote cannot be included.

One business week minimum from time of ballot start to ballot end.
Ballot end date declared at ballot start time by the admin.

Minimum of 9 "eligible" voters for a ballot to be declared valid, same rules for passage of an email ballot as a regular ballot. (Attend 2 working group meetings, or sufficent email participation to be knowledgeable about the proposal makes you eligible to vote.)

All email votes to be affirmed at the next official working group meeting which has a quorom in attendence, so they make it into the official working group minutes. The admin presents a list of email items and there votes and status at each working group meeting. (Note: the intention here is a vote to affirm the email ballots and results and not to generate technical discussion of email approved/rejected items at the working group meeting.)

The admin posts the results of email ballots in the proposals area on playground.sun.com.

The admin can declare certain periods of the year (holiday periods and periods surrounding official working group meetings as ineligible for email balloting. Initially, Dec 15 - Jan 7 are declared as ineligible and the week before and the week of a working group meeting are declared as ineligible. The admin can appoint a temporary administrator to handle email balloting for periods the admin will be on vacation, or declare those periods as ineligible for email balloting as well.

-David

[P1275 Item #250 -- Received: Mon Mar 13 18:41:41 PST 1995]

Date: Thu, 29 Feb 1996 13:00:38 +0000
From: Ron Hochsprung <ron@apple.com>
Subject: Item #322: RP: Forth source & FCode image ...

[ADMINISTRATIVE NOTE: Improperly encoded binary attachment has been deleted from this file.]

P1275 Openboot Working Group Proposal -- Proposal #:322 Ver 1

Title: Forth source & FCode image support

Author: Ron Hochsprung

Date: 2/29/96

Ed/Tech: Technical

Synopsis: init-program support for Forth source and FCode images

Doc & Version: Forth source & FCode image support

Problem: this is the text for support previously discussed

Proposal: I am attaching the .pdf file of the proposed recommended practice.

Ron

[ADMINISTRATIVE NOTE: Improperly encoded binary attachment has been deleted from this file.]

[P1275 Item #322 -- Received: Thu Feb 29 12:58:41 PST 1996]

Date: Tue, 2 Jul 1996 20:42:05 -0700
From: dmk@uask4it-223 (David Kahn)
Subject: Item #370: 64-bit Extensions: FCode32 Compatibility

P1275 OpenFirmware Working Group Proposal -- Proposal #:370 Ver 1

Title: 64-Bit Extensions: Fcode32 Compatibility

Author: David Kahn

Date: 2 July 1996

Ed/Tech: Technical

Synopsis: Extend IEEE p1275.6 to allow for 32 bit FCode compatibility.

Doc & Version: 64 Bit Extensions, p1275.6/D5

Problem: The 64 bit extensions supplement needs to deal with compatibility for the existing wealth of 32 bit FCode.

Draft 5 largely ignored the unfortunate fact that many existing FCode programs will not work without assistance in a 64 bit cell-sized environment. The reasons are varied; sometimes the FCode programs have incorrect assumptions that the cell size is 4; other cases are that simply sign-extending or zero-extending 32 bit values won't work.

Proposal:

Create a basic 32 bit environment for FCode, but allow for 64 bit register operations using lxjoin xlsplit (and similar words) with rx! and rx@ for the few devices in the future that may depend on a 64 bit environment.

Create a new section 6 in the document with the following text:

6. FCode Environment

Since existing FCode is written for a 32 bit environment, The FCode interpreter for 64 bit systems must provide a 32-bit clean environment to allow existing 32-bit FCode to work.

The following FCode's have special semantics in 64-bit environments. The semantics are modified as noted below in the following table. Note that the FCode implementation does not affect any other interface, thus the existing semantics in the user interface are unchanged.

New Definitions:

These definitions extend the definitions in Section A.1.2.2 in the Core specification:

l-addr: a 32-bit address, normalized by the implementation. The implementation shall ignore the most-significant 32-bits of the address.

l-qaddr: a quad-aligned 32-bit address, normalized by the implementation. The implementation shall ignore the most-significant 32-bits of the address.

l-waddr: a doublet-aligned 32-bit address, normalized by the implementation. The implementation shall ignore the most-significant 32-bits of the address.

lval, lvalxyz: a quadlet, normalized by the implementation. (arbitrary number) The stack item is an arbitrary 32-bit value. The most significant 32-bits of the stack item are ignored.

ln, lnxyz: a quadlet, sign-extended by the implementation. The stack item is a signed 32-bit value, the most significant 32-bits of the stack item are ignored.

ul, ulxyz: Unsigned arbitrary quadlet; the implementation shall ignore the most-significant 32-bits of the value. The stack item is an unsigned 32-bit number.

(Ed note: Where the 'interpret as' refers to an existing definition, no stack diagram is shown. The document will include short descriptions of each item. For the "interpret as" column, names not given in the standard will either be defined in the document, or not used in the description.)

```
FCode          Interpret as:   Stack Diagram:
=====          =============   ==============

,              l,              Core Definition Applies.
/n             /l              Core Definition Applies.
na+            la+             Core Definition Applies.
cell+          la1+            Core Definition Applies.
cells          /l*             Core Definition Applies.

+!             l+!             ( nu l-addr -- )
>>a            l>>a            ( q1 u -- q2 )
rshift         lrshift         ( q1 u -- q2 )

b?branch       lb?branch       ( ln-dont-branch? -- )
                               ( F: /Fcode-offset32/ -- )

b(of)          lb(of)          ( ln-sel ln-of-val -- sel|<nothing> )
                               ( F: /Fcode-offset32/ -- )

b(do)          lb(do)          ( ln-limit ln-start -- )
                               ( F: /Fcode-offset32/ -- )

b(?do)         lb(?do)         ( ln-limit ln-start -- )
                               ( F: /Fcode-offset32/ -- )

b(+loop)       lb(+loop)       ( ln-delta -- )
                               ( F: /Fcode-offset32/ -- )

0=             l0=             ( lval | flag -- equal-to-0? )
0<>            l0<>            ( lval -- not-equal-to-0? )
0<             l0<             ( lval -- less-than-0? )
0<=            l0<=            ( lval -- less-or-eq-to-0? )
0>             l0>             ( lval -- greater-than-0? )
0>=            l0>=            ( lval -- gt-or-eq-to-0? )
<              l<              ( lval1 lval2 -- less? )
>              l>              ( lval1 lval2 -- greater? )
=              l=              ( lval1 lval2 -- equal? )
```

```
<>          l<>         ( lval1 lval2 -- not-equal? )
>=          l>=         ( lval1 lval2 -- gt-or-eq? )
<=          l<=         ( lval1 lval2 -- lt-ot-eq? )

u>          lu>         ( ul1 ul2 -- unsigned-gt? )
u<=         lu<=        ( ul1 ul2 -- unsigned-lt-or-eq? )
u<          lu<         ( ul1 ul2 -- unsigned-less? )
u>=         lu>=        ( ul1 ul2 -- unsigned-gt-or-eq? )

between     l-between   ( lval min max -- min<=lval<=max?)
within      l-within    ( lval min max -- min<=lval<max?)
max         l-max       ( lval1 lval2 -- lval1 | lval2 )
min         l-min       ( lval1 lval2 -- lval1 | lval2 )
abs         l-abs       ( lval -- u )

@           l-@         ( l-addr -- q ) Fetches a quadlet
!           l-!         ( q l-addr -- ) Stores a quadlet
2@          l2@         ( l-addr -- q1 q2 ) Fetch 2 quadlets
2!          l2!         ( q1 q2 l-addr ) Stores 2 quadlets
l@          ll@         ( l-addr -- q )
l!          ll!         ( q l-qaddr -- )
<w@         l<w@        ( l-waddr -- n )
w@          lw@         ( l-waddr -- w )
w!          lw!         ( w l-waddr -- )
c@          lc@         ( l-addr -- byte )
c!          lc!         ( byte l-addr -- )

on          lon         ( l-qaddr -- ) Stores a quadlet
off         loff        ( l-qaddr -- ) Stores a quadlet
base        lbase       ( -- qaddr )
#out        l#out       ( -- qaddr )
#line       l#line      ( -- qaddr )

rl!         lrl!        ( q l-qaddr -- )
rl@         lrl@        ( l-qaddr -- q )
rw!         lrw!        ( w l-waddr -- )
rw@         lrw@        ( l-waddr -- w )
rb!         lrb!        ( byte l-addr -- )
rb@         lrb@        ( l-addr -- byte )

move        l-move      ( l-addr-src l-addr-dest len -- )
fill        l-fill      ( l-addr len byte -- )
cpeek       lcpeek      ( l-addr -- false | byte true )
wpeek       lwpeek      ( l-waddr -- false | w true )
lpeek       llpeek      ( l-qaddr -- false | q true )
cpoke       lcpoke      ( byte l-addr -- okay? )
wpoke       lwpoke      ( w l-waddr -- okay? )
lpoke       llpoke      ( q l-qaddr -- okay? )
```

[P1275 Item #370 -- Received: Tue Jul 2 20:43:04 PDT 1996]

From: kingman@austin.ibm.com (Kingman)
Subject: Item #387: Administrative: OFWG Minutes for August Meeting
Date: Thu, 22 Aug 1996 15:19:36 -0500 (CDT)

P1275 Open Firmware Working Group Proposal -- Proposal #387 Ver <Auth-Vers#>

Title: Minutes of August Open Firmware Working Group meeting

Author: John Kingman

Date: August 22, 1996

Attached please find the minutes of our last meeting held August 13, and August 14, 1996 at Apple, Cupertino, CA. Thanks to Ron for making the arrangements.

Also please note that: (1) our next meeting will be at Motorola in Tempe, AZ, on October 8-9, 1996 and (2) check the "Action Item Summary" at the end of this note for your action items.

Regards,

John Kingman, Secretary Open Firmware Working Group
kingman@austin.ibm.com

++

1- Attendees

John Kingman	IBM RS/6000 Div. Secretary OFWG	512-838-1546	kingman@austin.ibm.com
Chris Bellman	Adaptec	408-945-8600	cbellman@corp.adaptec.com
Jordan Brown	Sunsoft	310-348-6039	jordan.brown@west.sun.com
Greg Hill	Firmworks	415-917-6985	GREGH@FIRMWORKS.COM
Ron Hochsprung	Apple/Sys. Arch.	408-974-2661	ron@apple.com
John Hall	Sun	415-786-6927	JDHall@eng.sun.com
Bob Coffin	IBM	512-838-8240	coffin@vnet.ibm.com
John Dickol	IBM	512-838-6005	dickol@vnet.ibm.com
Thomas J. Merritt	CodeGen	415-346-8762	tjm@cgt.com
David Paktor	Sun	408-774-8306	Paktor@SUN.com
Ping Lee	Apple	408-974-6956	pingl@apple.com

First time attendees:

Chris Maloney	Motorola	602-437-6716	CMaloney@phx.mcd.mot.com
Michael Milendorf	Sun	508-442-0803	michael.milendorf@east.sun.com

Part time attendees:

Conrad Minshall	Apple	408-252-3377	conrad@apple.com
Mitch Bradley	Firmworks	415-917-6984	wmb@firmworks.com

2- Review Agenda

Proposed agenda (#386) was reviewed and approved unanimously with right of revision to the Secretary.

3- Administrative/Procedural items

None raised.

4- Last meeting minutes review

```
   Voted to approve the minutes of the meeting of June 25, 1996 (#356)
   (unanimous)

5- Future Meetings
   ===============

   October 8 - 9, 1996          Motorola, Tempe, AZ
                                Host: Rob Baxter

   January 14 - 15, 1997        TBD, Salt Lake City, UT (?)
                                Host: David Kahn to arrange

   March 4 - 5, 1997            IBM, Austin, TX
                                Host: John Dickol

6- Standard Progress Report
   ========================

   No new status.

7- Action Items Review
   ===================

   -2-  open   - skip
   -4-  open   - no change
   -6-  closed - new PCI binding available
   -11- open   - no change
   -12- open   - no change
   -26- open   - no change

8- Other Pending/Tabled items
   ==========================

   #250  (Email balloting procedure)
        -- open - skip
   #322  (Forth source and FCode image support)
        -- open - no change
   #323b (change to *output* definition)
        -- withdrawn, JB to make a proposal with new wording.
   #336  (posting of Generic Names 1.1)
        -- closed - superceded

9- New Proposal Status summary
   ===========================

   #369 -- (PCI) Approved
   #370 -- (64-bit) Open, tabled
   #374 -- (Minutes) Closed, Accepted
   #375 -- (SCSI-3 Parallel) Tabled
   #376 -- (CHRP) Remanded to subcommittee
   #377 -- (CHRP) Remanded to subcommittee
   #378 -- (TFTP) Approved as amended
   #379 -- (TFTP) Approved as amended
   #380 -- (PCI) Approved as amended
   #381 -- (PC kbmc) Approved
   #382 -- (PC kbd) Approved as amended
   #383 -- (base) Withdrawn
   #384 -- (PC serial) Approved as amended
   #386 -- (Agenda) Closed, Accepted

10- Document Reviews (including associated proposals, if any)
    ================

  PCI Binding
  -----------
  Revision 2.0
  #369 (available property for bus nodes)
      -- approved unanimously
  #380 (Include pci class code string in 'compatible')
      -- approved as amended unanimously
      Amendments:
      - form of name shall be "pciclass,NNNNNN"
      - NNNNNN shall be lower case hex including leading zeroes.
      - delete the last sentence of the paragraph

      Editor directed to create Approved Version 2.1 11-0-0

TFTP Booting Recommended Practice
---------------------------------
  Version 0.7
  #378 (Property Values)
      -- approved as amended
      Amendments:
      - delete "15pin" wherever it occurs
      - update fddi connectors
  #379 (Open Argument values)
      -- approved as amended (1 abstension)
      Amendments:
      - change target document to DSE (add Network section)
      - move section 5 from TFTP to DSE

Base
----
  #383 (clarifications on MEMORY-TEST-SUITE and MASK)
      -- withdrawn by author; author to submit a clarification.
      Comments:
      - memory-test-suite may not work for PCI memory on a big endian
        platform.  Need statements somewhere about situations in which
        memory-test-suite is likely not to work and guidance for making
        an endian independent memory-test-suite.
      AI for WMB/DMK to take a to look at 64-bit MASK

SCSCI-3 Parallel Interface Binding
----------------------------------
  #375 (proposal of Draft 1)
      -- Tabled
      - The attendees wished to defer discussion until Mitch Bradley
        and/or David Kahn were present.

PC keyboard/mouse controller binding
------------------------------------
  #381 (proposal of Version 0.3)
      -- approved
  The editor is directed to produce Approved Version 1.0

PC keyboard binding
-------------------
  #382 (proposal of Version 0.3)
      -- approved with amendments
  Amendments:
      - fix [3] "???"
      - fix "??" at XT keyboard in section 4
      - add references for ANSI/ISO
      - mention "Windows 95" keyboard in section 5
      - section 6 -- delete bracketed text -- "none" for now
      - end of 7.2.1 delete "or empty"
      - 7.2.2 keyboard indicators should match
      - copy abort sequence from DSE to this doc.
      - specify open arguments
```

```
    - Move list of layouts in 7.1.2 to an appendix/registry.
  The editor is directed to produce Approved Version 1.0

 PC Serial Port Device Binding
 -----------------------------
  #384 (proposal of Version 0.99)
      -- approved with amendments 10-0-1
  Amendments:
      - move 7.2 contents to DSE
      - strike bracketed note at 9.1
      - 9.1 last para -- change "devices which are" to "devices
        that are"
      - change "pnp500's" to "may" instead of "should"
  The editor is directed to produce Approved Version 1.0

11- Action Items Summary
    ====================

    The following AIs are still open:
```

No.	Who	Description
2	Editors of Recommended Practice documents	Update document template and contents per discussion.
4	Greg H. John H.	Standard properties document.
11	Jordan B.	Reorganize discussion of probing in ISA binding
12	Jordan B.	Suggest wording for IRQ 2 vs 9
26	David K.	Meeting details for January

New AIs:

No.	Who	Description
27	Jordan B.	Propose replacement for #323b (withdrawn)
28	Ron H.	Make a proposal for PCI hot plug
29	Mitch B. David K.	Do we need to clarify MASK in 64-bit mode for (mem-test-suite)?
30	Jordan B.	Make a proposal for a registry for lists of supported things (e.g., keyboards, network types, etc.)

```
++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++

SUBCOMMITTEE MINUTES:

Attendees
=========

John Kingman      IBM RSD           512-838-1546 kingman@austin.ibm.com
Ron Hochsprung    Apple/Sys. Arch.  408-974-2661 ron@apple.com
Bob Coffin        IBM               512-838-8240 coffin@vnet.ibm.com
Greg Hill         Firmworks         415-917-6985 GREGH@FIRMWORKS.COM
Jordan Brown      Sunsoft           310-348-6039 jordan.brown@west.sun.com
John Dickol       IBM               512-838-6005 dickol@vnet.ibm.com
Ping Lee          Apple             408-974-6956 pingl@apple.com
David Paktor      Sun               408-774-8306 Paktor@SUN.com
Chris Maloney     Motorola          602-437-6716 CMaloney@phx.mcd.mot.com
Michael Milendorf Sun               508-442-0803 michael.milendorf@
                                                 east.sun.com

Review Agenda
=============

 Agenda:
 - establish quorum
 - review minutes
 - review AIs
 - review documents and proposals

Establish Quorum
================

 Quorum established

Review minutes of last meeting
==============================

 Voted to approve the minutes of the subcommittee meeting of June 26,
 1996 (#374). (unanimous)

Action Item Review
==================
 -S2- Open   - no change.
 -S4- Open   - no change.
 -S5- Open   - no change.
 -S8- Open   - no change.
 -S9- Closed - Item #330 rejected for lack of rationale
-S10- Closed - New PreP binding available.
-S11- Closed - New Generic Names RP available.
-S12- Closed - New 8042 device binding available.
-S13- Closed - New serial device binding available.
-S14- Closed - Jordan unable to come up with a "name" convention for
               superio chips.

Review Documents and Proposals
==============================

  Device Support Extensions version 0.7, 0.7+
  -------------------------------------------
        #350 ("sound" device properties)
                (proposal was previously tabled)
            - Discussion of sample-frame-sizes
                RC and JB to resolve
            - Missing sample rate (8 KHz)
            - possibly add a figure to help (AI for JB)
        Serial -- check about what "Not supported" means
        Network -- added section (0.7+)
            - Say something about passing unrecognized parms to TFTP
                (Open arguments, etc., check with MB)
            - Add in section 9 what you read and write (GH AI)
            - add network to 13 (/aliases)
    Produce 0.8 for next meeting

  CHRP Device bindings version 1.0
  --------------------------------
        #359 (proposal for CHRP device bindings)
        -- Closed administratively
```

```
        - proposals to review individual bindings will be
          submitted, as appropriate.
      #361 (CHRP 8259: add "reserved-interrupts" property)
      -- withdrawn by author
         - updated binding to be submitted

Generic Names Version 1.2
-------------------------
      Suggested changes:
      - Update section 3 - names and "shall" in guideline 2
      - Move section 6 info to PCI binding
      - Make section 7 "(Informative)"
              line 46 insert "if" before "software"
              line 55 "identity" misspelled
  Produce 1.3 for next meeting

ISA/EISA/ISA-PnP Binding Version 0.3
------------------------------------
      Suggested changes:
      - Add slot-index as a child property to be used with slot-names
      - Update compatible as follows to include the "FFF" to denote
        which logical function this node is related to.

              1a) If the device has multiple logical devices:
                      pnpVVV,PPPP,FFF
                  where
                      VVV,PPPP is the PnP vendor ID for the card
                      FFF is the number of the logical device with
                      0 denoting the first logical device, 1 denoting
                      the second, and so on.

              1b) If the device has only a single logical device:
                      pnpVVV,PPPP
                  where
                      VVV,PPPP is the PnP vendor ID for the card

              2)  If a logical device ID is supplied:
                      pnpVVV,PPPP
                  where
                      VVV,PPPP is the PnP logical device ID.

              3)  If one or more PnP "compatible ID" record is supplied,
                  then for each record in order:
                      pnpVVV,PPPP
                  where
                      VVV,PPPP is the PnP compatible ID.

      - page 16, line 9 spelling of "encode"
  Produce 0.4 for next meeting

CHRP System Binding
-------------------
  #357 (Revision 1.5 posting)
      -- closed administratively
  #360 (Genericize Open PIC controller name)
      -- withdrawn by author (included in latest revision)
  #376 (chosen nvram)
      -- approved as amended:
              - add to the note in DSE that the size returned by the size
                method will be the platform defined nvram size. OF/OS
                should only address nvram from 0 up to this size-1.
  #377 (internal (dummy) nodes)
      -- approved as amended:
              - change to "internal" to "reserved"
              - add device_type of reserved

    Revision 1.6
        Suggested changes:
                - delete lines 13-21 on page 32
                - change "property" to "node" on line 30
                - Make ICU "should" instead of "shall" -- may need an ACR
                - PE format discussion (16MB is new req.) -- may need an ACR
                - add "encode-unit" and "decode-unit" to Section 5.1.3.2 (ROM
                  Node Methods) and include arguments for "set-args"
                - clarify Section 5.1.4.1 (ROM Child Node Properties) that
                  "reg" specifies the ROM base address and length.
    Produce 1.7 (Approved Version) for next meeting

  Interrupt Mapping Recommended Practice Version 0.9
  --------------------------------------------------
        Suggested change:
        - Note that if the "interrupt-map" property is absent for an interrupt
          parent node that the identity map is to be used.
    Produce 1.0 (Approved Version) for next meeting


Action Items Summary
====================

Old AIs still open:
```

No.	Who	Description
S2	Ron H.	Update and create v1.1 Forth and FCode Rec Practice
S4	Bob C.	Make "shared-bus-master" proposal
S5	Ron H.	Make "chording" (keyboard) proposal
S8	Mitch B.	Propose that debug facilities not be supported in real-mode.

New AIs:

No.	Who	Description
S14	Jordan B.	Create a picture to explain sound device terminology.
S15	Greg H.	Provide material for TFTP section 9 describing what is read and written
S16	Bob C. Jordan B.	Migrate PReP source to playground
S17	Bob C. Jordan B.	Resolve sample-frame-sizes description

```
++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++++

[ P1275 Item #387 -- Received: Thu Aug 22 13:18:36 PDT 1996 ]
```

```
From: kingman@austin.ibm.com (Kingman)
Subject: Item #388: Request email balloting of Item #250
Date: Fri, 23 Aug 1996 08:54:32 -0500 (CDT)

P1275 Open Firmware Working Group Proposal -- Proposal #388 Ver <Auth-Vers#>

Title:          Request for email balloting of Proposal 250

Author:         John Kingman

Date:           August 23, 1996

Ed/Tech:        Procedural

Synopsis:       It's time we had an email balloting procedure.

Proposal:

Proposal #250 indicates that any eligible member can request email balloting
of a properly submitted proposal.  Since Proposal #250 was submitted properly,
I am requesting email balloting of it, to test out the procedure.

John Kingman

[ P1275 Item #388 -- Received: Fri Aug 23 06:53:19 PDT 1996 ]
```

```
Date: Tue, 3 Sep 96 10:31:35 PDT
From: wmb@FirmWorks.COM (Mitch Bradley)
Subject: Item #389: Core: Stack diagram error in endcase and b(endcase)

P1275 Open Firmware Working Group Proposal -- Proposal #389 Ver <Auth-Vers#>

Title:          Stack diagram error in endcase and b(endcase)

Author:         Mitch Bradley

Date:           September 9, 1995

Ed/Tech:        Technical

Synopsis:       The stack diagram for endcase and b(endcase) is wrong

Doc & Version:  1275-1994

Problem:        The run-time stack diagram for endcase and b(endcase)
                in 1275 disagrees with the stack diagram for ENDCASE
                in ANS Forth.  It should be ( sel -- ), not
                ( sel | <nothing> -- ).  The <nothing> case does not
                exist, because endof transfers control to the point
                just *after* endcase.  The run-time action of endcase
                executes only when no "of" clause succeeded, i.e. the
                default case.  I think this is just a documentation
                error, because I'm not aware of any implementation that
                does the wrong thing.

Proposal:

1) On the Web page, move the link to the "core errata" document so that it
   appears near the link to the core document.

2) Create an on-line document named "additional errata", with a link located
   just after the link to the core errata document.  Add to that document
   the text in the "Problem" section above.

3) If and when we do another revision of the IEEE core errata document,
   incorporate the contents of the on-line additional errata document
   into the IEEE document.

[ P1275 Item #389 -- Received: Tue Sep  3 10:29:50 PDT 1996 ]
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From: kingman@austin.ibm.com (Kingman)
Subject: Item #390: PPC: reserved area size increase
Date: Tue, 24 Sep 1996 10:16:16 -0500 (CDT)

P1275 Open Firmware Working Group Proposal -- Proposal #390 Ver <Auth-Vers#>

Title:          Increase the memory reserved for OF on 64-bit systems.

Author:         John Kingman

Date:           September 24, 1996

Ed/Tech:        Technical

Synopsis:       Increase the memory reserved for OF on 64-bit systems.

Doc & Version:  PowerPC Processor Binding, Revision 2.0

Problem:        The current 32-byte area is insufficient on 64-bit
                systems, since the size of the registers have doubled.
                This area may be used for saving registers and executing
                a portion of the interrupt routine.   Since the registers
                are 8-bytes instead of 4-bytes in size, additional space
                is needed.  An extra 16-bytes is propsed for 64-bit
                system implementations.

Proposal:

Section 8.4 on page 22, line 19, add the following to the end of the
sentence: "or 0x1d0 to 0x1ff on 64-bit implementations."

[ P1275 Item #390 -- Received: Tue Sep 24 08:18:47 PDT 1996 ]
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From: Michael Segapeli <mikes@austin.ibm.com>
Subject: Item #391: DSE: Clarify Arguments to network Devices
Date: Mon, 30 Sep 1996 17:39:43 -0500

P1275 Open Firmware Working Group Proposal -- Proposal #391 Ver <Auth-Vers#>

Title:          Clarify Arguments to network Devices

Author:         Mike Segapeli

Date:           September 30, 1996

Ed/Tech:        Technical

Synopsis:       The format of the open arguments are not clear

Doc & Version:  Device Support Extensions Version 0.8

Problem:        The Device Support Extensions defines three arguments to the
                "open" method for network devices.  The "TFTP Booting
                Recommended Practice" defines arguments to the "open" method
                of the OBP-TFTP support package.  Both sets of arguments
                are passed via the device's "open" method.  However it is
                not clear which set comes first.

                Also For the arguments "promiscuous, speed and duplex" it is
                not clear whether the delimiting commas are needed if one of
                these arguments is not needed.

Proposal:       In section 9.2 replace:

                   "open network-device:[promiscuous,][speed=n,][duplex=mode,]"

                with

                   "The open arguments to a network device are defined as
                    follows:

                    open network-device:[promiscuous],[speed=n],[duplex=mode],
                                        [bootp],[siaddr],[filename],[ciaddr],
                                        [giaddr],[bootp-retries],[tftp-retries]
"


                Add the following sentence at the front of the paragraph
                starting at line 47.

                   "The open method of the device shall parse the first three
                    arguments and pass the remaining arguments to the OBP-TFTP
                    support package's "open" method."


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Michael Segapeli                  (512) 838-8428  (T/L 678-8428)
IBM RISC System/6000 Division     Internet: mikes@austin.ibm.com




[ P1275 Item #391 -- Received: Mon Sep 30 15:38:50 PDT 1996 ]
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From: kingman@austin.ibm.com (Kingman)
Subject: Item #392: Administrative: Agenda for October OFWG Meeting
Date: Wed, 2 Oct 1996 09:34:57 -0500 (CDT)

P1275 Open Firmware Working Group Proposal -- Proposal #392 Ver <Auth-Vers#>

Title:          OFWG Agenda for October Meeting

Author:         John Kingman

To:             Open Firmware Working Group Members,

Attached is the agenda for the Open Firmware Working Group meeting to be
held at Motorola, Tempe, AZ on Tuesday October 8, 1996 and Wednesday
October 9, 1996. Directions and hotel information are available at:

        http://playground.sun.com/pub/1275

Please check below for any Action Items assigned to you. For those with
Action Items who cannot come to the meeting, please send me the status of
your item(s) by e-mail by 8:00AM Central Time Monday, October 7, 1996.

Thanks.

John Kingman, Secretary Open Firmware Working Group
kingman@austin.ibm.com

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Times are meeting location local time:

Tuesday October 8, 1996

     9:15AM     - Meet at Motorola
                   Light breakfast to be provided by Motorola.
     9:30AM  1 - Introduction
             2 - Review Agenda
             3 - Administrative/Procedural Items
             4 - Review and approve minutes from the last meeting
             5 - Future Meetings
             6 - Standard Progress Report
             7 - Action Items Review (see note below)
             8 - Other Pending/Tabled items
    12:00M      - Break for LUNCH
     1:00PM  9 - Proposal Status Review
            10 - Document Reviews (including associated proposals, if any)
                  64 Bit Extensions, p1275.6/D5
                  Generic Names Practice
                  Forth source & FCode image support Practice
                  SCSI-3 Parallel Interface (SPI) Binding
     5:00PM     - Break for the day

Wednesday October 9, 1996

     9:15AM     - Meet at Motorola
                   Light breakfast to be provided by Motorola.
     9:30AM 10 - Document Reviews (cont'd)
                  ISA/EISA/ISA-PnP Binding
                  Power PC Processor Binding
                  CHRP System Binding
                  Device Support Practice
    12:00M      - Break for lunch
     1:00PM 10 - Document Reviews (cont'd)
                  Others?
     2:30PM     - Summarize/Assign action items and Wrap up
     3:00PM     - Adjourn

NOTES:
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Action Items:
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No.	Who	Description
2	Editors of Recommended Practice documents	Update document template and contents per discussion.
4	Greg H. John H.	Standard properties document.
11	Jordan B.	Reorganize discussion of probing in ISA binding
12	Jordan B.	Suggest wording for IRQ 2 vs 9
26	David K.	Meeting details for January
27	Jordan B.	Propose replacement for #323b (withdrawn)
28	Ron H.	Make a proposal for PCI hot plug
29	Mitch B. David K.	Do we need to clarify MASK in 64-bit mode for (mem-test-suite)?
30	Jordan B.	Make a proposal for a registry for lists of supported things (e.g., keyboards, network types, etc.)

Subcommittee Action Items:

S2	Ron H.	Update and create v1.1 Forth and FCode Rec Practice
S4	Bob C.	Make "shared-bus-master" proposal
S5	Ron H.	Make "chording" proposal
S8	Mitch B.	Propose that debug facilities not be supported in real-mode.
S14	Jordan B.	Create a picture to explain sound device terminology.
S15	Greg H.	Provide material for TFTP section 9 describing what is read and written
S16	Bob C. Jordan B.	Migrate PReP source to playground
S17	Bob C. Jordan B.	Resolve sample-frame-sizes description

[P1275 Item #392 -- Received: Wed Oct 2 07:34:25 PDT 1996]